

# Refine Search

## Search Results -

Terms	Documents
(semiconductor and (moulding adj layer) and (storage adj (node or nodes))).clms.	0

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

Refine Search

Recall Text

Clear

Interrupt

## Search History

DATE: Wednesday, April 19, 2006 [Printable Copy](#) [Create Case](#)

### Set Name Query

side by side

Hit Count Set Name  
result set

DB=PGPB; PLUR=YES; OP=ADJ

L7 (semiconductor and (moulding adj layer) and (storage adj (node or nodes))).clms.

0 L7

DB=JPAB; PLUR=YES; OP=ADJ

L6 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

0 L6

DB=EPAB; PLUR=YES; OP=ADJ

L5 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

0 L5

DB=USPT; PLUR=YES; OP=ADJ

L4 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

18 L4

DB=PGPB; PLUR=YES; OP=ADJ

L3 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

14 L3

DB=TDBD; PLUR=YES; OP=ADJ

L2 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

0 L2

DB=DWPI; PLUR=YES; OP=ADJ

L1 semiconductor and (moulding adj layer) and (storage adj (node or nodes))

9 L1

END OF SEARCH HISTORY